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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10063472	FILING DATE 04/26/2002	CLASS 324	SUBCLASS 7654	GAU 2132 2858	EXAMINER J. KERVEROS
**APPLICANTS: Ozaki Hideharu; <div style="text-align: center;">714 2620 2133</div>					
**CONTINUING DATA VERIFIED: <i>none</i>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-229629 07/30/2001 <div style="text-align: center;">Yes 1/29/04</div>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <i>Yes 1/29/04</i>		ATTORNEY DOCKET NO 15483	
TITLE : Semiconductor integrated circuit device and device for testing same					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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